

FEB 28 2007

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**FROM:** Thomas Chan**DATE:** February 28, 2007

Number of pages with cover page:	9	Originals Will Not Follow
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Preparer of this slip has confirmed that facsimile number given is correct: 12923/lca1**Comments:****INITIAL FILING**

Attorney Docket No.: 333772000900

Group Art Unit: 2863

Examiner: S. Kundu

Serial No.: 10/772,327

Filing Date: February 6, 2004

Inventor(s): Ankan PRAMANICK et al.

Title: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

**Papers enclosed herewith:**

Transmittal form (1 page)

Reply Brief (7 pages)

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<b>TRANSMITTAL FORM</b>		Application Number 10/772,327
<i>(to be used for all correspondence after initial filing)</i>		Filing Date February 6, 2004
		First Named Inventor Ankan PRAMANICK
		Art Unit 2863
		Examiner Name S. Kundu
Total Number of Pages in This Submission 8		Attorney Docket Number 333772000900

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Date	February 28, 2007	Reg. No. 51,543

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Docket No.: 333772000900  
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Ankan PRAMANICK et al.

Application No.: 10/772,327

Confirmation No.: 4514

Filed: February 6, 2004

Art Unit: 2863

For: **METHOD AND APPARATUS FOR TESTING  
INTEGRATED CIRCUITS**

Examiner: S. Kundu

**REPLY BRIEF**

MS Appeal Brief - Patents  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This Reply Brief is in response to the Examiner's Answer, dated January 31, 2007, for which a response is due on February 28, 2007. Accordingly, this response filed on February 28, 2007 is timely filed.

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**I. STATUS OF CLAIMS****A. Total Number of Claims in Application**

There are 24 claims pending in application.

**B. Current Status of Claims**

1. Claims canceled: 0
2. Claims withdrawn from consideration but not canceled: 0
3. Claims pending: 1-24
4. Claims allowed: 0
5. Claims rejected: 1-24

**C. Claims on Appeal**

The claims on appeal are claims 1-24.

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**II. GROUNDS OF REJECTION TO BE REVIEWED ON APPEAL**

- A. Whether claims 1-5 are anticipated under 35 U.S.C. 102(b) by U.S. Patent No. 6,028,439 (the Arkin reference).
- B. Whether claims 6-8, 24 are unpatentable under 35 U.S.C. 103(a) over the Arkin reference in view of US 2002/0183955 A1 (the Adler reference).
- C. Whether claims 9-14, 19 are unpatentable under 35 U.S.C. 103(a) over the Arkin and Adler, and further in view of US 2003/0167277 A1 (the Hejlsberg reference).
- D. Whether claims 15-18, 21-23 are unpatentable under 35 U.S.C. 103(a) over the Arkin reference.
- E. Whether claim 20 is unpatentable under 35 U.S.C. 103(a) over the Arkin, Adler, and Hejlsberg, and further in view of US Patent No. 6,782,336 (the Shah reference).

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### III. REPLY TO EXAMINER'S ANSWER

After filing the Reply Brief to the Examiner's Answer on January 22, 2007, Applicants' representative received a voicemail from the Examiner stating that the Examiner's Answer mailed on November 20, 2006 may be erroneous and a revised Examiner's Answer will be sent. However, upon reviewing the revised Examiner's Answer dated January 31, 2007 in detail, Applicants do not find any substantive changes in the Examiner's Answer other than a few minor changes in the headings. For example, under heading (6), "Issues" was changed to "Grounds of Rejection to be Reviewed on Appeal." Under heading (7), "ClaimsAppealed" was changed to "Claims Appendix." Under heading (8), "Prior Art of Record" was changed to "Evidence Relied Upon." Heading (11) was added. There are no substantive changes to the Examiner's Answer previously submitted on November 20, 2006.

Appellants respectfully request consideration of the following remarks in view of the Examiner's Answer on Appeal mailed January 31, 2007. Appellants maintain the arguments set forth in Appellants' Appeal Brief filed on September 25, 2006 and Applicants' Reply Brief to the Examiner's Answer dated January 22, 2007, which is repeated below because there is no change in substance from the Examiner's Answer mailed on November 20, 2006 and the Examiner's Answer mailed on January 31, 2007.

#### A) Applicants respectfully disagree with Examiner's interpretation of the site controller

In Figure 2, the Examiner identifies the computer bus (38A) as the site controller. Applicants respectfully submit that person skilled in the art would understand that a computer bus consists of a set of hardware lines (metal conductors) used for data transfer among the components of a computer system. **Such hardware lines have no intelligence and thus they are incapable of controlling other electronic components.** On the contrary, the computer bus (38A) needs to be controlled by other electronic components, such as the bus controller (35), as shown in Figure 2 of the Arkin reference.

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In the present case, independent claim 1 recite, in part, "at least one local operating system associated with each site controller for enabling control of at least one test module by an associated site controller" and "the associated site controller controls at least one test module interactively". Based on plain language interpretation of these claim elements, **Applicants submit that person skilled in the art would agree with the Applicants that the set of hardware lines would not be able to control the at least one test module interactively.** In addition, Applicants submit that person skilled in the art would not speak of a local operating system to be associated with a set of hardware lines as proposed by the Examiner.

**B) Applicants respectfully disagree with Examiner's interpretation of the system controller, site controller, and test module of the present invention**

On page 10 of the Examiner's Answer, the Examiner indicated that the test module contains the system controller and the site controller. In other words, both the system controller and the site controller are embedded in the test module. Applicants respectfully remind the Board that the Arkin reference clearly shows that "FIG. 2 illustrates a typical tester module of FIG. 1 in more detailed block diagram form" (See Arkin, C4:22-23.) Moreover, Arkin clearly indicates that FIG.2 is the test module 14(1) as labeled at the lower left corner of FIG. 2.

Claim 1 recites, in part, "a host operating system for enabling control of at least one site controller by a system controller" and "...enabling control of at least one test module by an associated site controller". Applicants respectfully submit that person skilled in the art would not consider the system controller and the site control to be embedded in the test module from reading the plain language of claim 1 and Figure 2 of the specification. Person skilled in the art would understand that a test module created by one vendor may not be able to interface with another test module created by a different vendor. Thus, **person skilled in the art would understand that the system controller and/or the site controller are not part of the test module, because the system controller and/or the site controller would be required to interface with different test modules provided by different vendors.**

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**C) Applicants respectfully disagree with Examiner's interpretation of "plug-and-play"**

On page 11 of the Examiner's Answer, the Examiner cites the specification which merely describes an implementation of plug-and-play of the present invention. However, the Examiner avoided responding to the key issue identified by the Applicants throughout the prosecution that a **test module is different from a device-under-test (DUT)**. Even FIG. 1 of the Arkin reference supports the Applicants' position as it identifies the test modules being items 14(1), 14(2), and 14(3), and the DUT(s) as item 12. Because the test modules and the DUT(s) are totally different entities, Applicants submit that the ability to attach/detach the DUT(s) does not anticipate the ability to attach/detach the test modules.

In addition, if the Examiner's position of the site controller is part of the test module (see Examiner's label of FIG.2 of Arkin) is adopted, Applicants submit that it is impossible to attach or detach the test modules from the site controller in a plug-and-play manner as required by the pending claim 1 (How can one separate the computer bus (38A), which is part of the test module, from the test module 14(1) according to the definition by the Examiner?).

**D) Response to Examiner's Additional Remarks**

Applicants agree that the pending claims must be "given the broadest reasonable interpretation consistent with the specification." However, Applicants submit that using a set of hardware lines (computer bus) to anticipate the functions of the site controller of the pending application is not a "reasonable interpretation consistent with the specification." Many computer systems contain multiple sets of hardware lines (computer buses). If a set of hardware lines (computer bus) is given the interpretation to anticipate any functionality (such as the site controller), then a computer system can almost anticipate any inventive electronic apparatus.

In addition, Applicants would like to bring the attention to the Board that Applicants added the clarification to claim 1 of the pending application regarding the interaction between the site controller and the test module, and added the limitation common clock according to the Examiners'

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suggestions on the April 13, 2006 interview that these limitations would distinguish the pending claims from the Arkin reference.

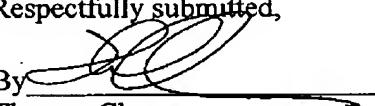
### CONCLUSION

For the foregoing reasons, Appellants assert that the Examiner's rejections of claims 1-24 are erroneous and that the claims are patentable. Reversal of the rejections is therefore respectfully requested.

The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.17 that may be required by this Reply Brief to the Examiner's Answer, or to credit any overpayment, to **Deposit Account No. 03-1952** referencing attorney docket no. 333772000900.

Dated: February 28, 2007

Respectfully submitted,

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